

FIGURE 1

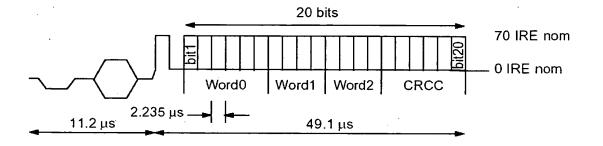


FIGURE 2

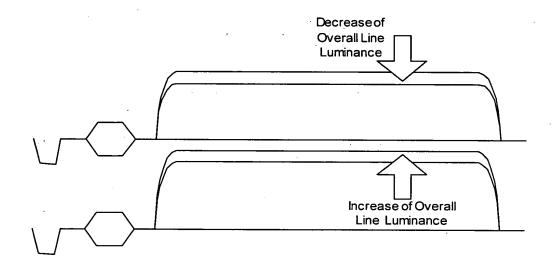


FIGURE 5

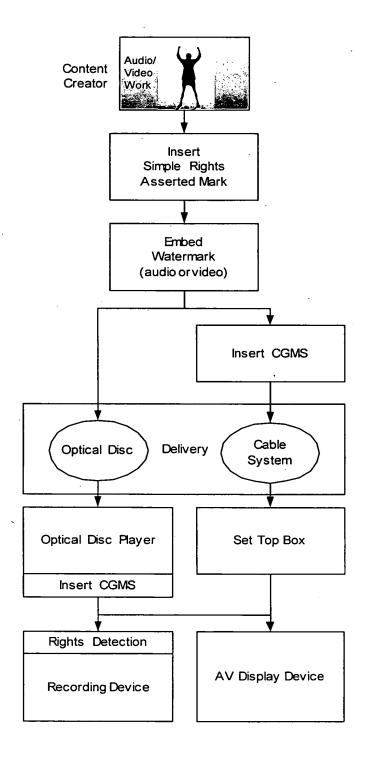


FIGURE 3

CGMS-A	RAM	Action	Redistribution				
Copy Freely	Not present	Copying allowed	RC bit, else not controlled				
Copy Freely	Present	Encrypted copy permitted	RC bit, else not allowed				
Copy Once	Don't care	Encrypted "Copy No More" copy permitted	RC bit, else not allowed				
Copy No More	Don't care	Copy not permitted, display only	RC bit, else not allowed				
Copy Never	Don't care	Copy not permitted, display only	RC bit, else not allowed				
Not present	Not present	Copying not controlled	Not controlled				
Not present	Present	"Copy Never", copy not permitted, display only	Not allowed				

Figure 4A

_	_	_	_	_	-		_	 _	_	_			_		_		_	
Meaning	Copy to be Encrypted?	with VRAM	(No Copies)	Yes	(No Copies)	Yes	(No Copies)	(No Copies)	No	(No Copies)	Yes	(No Copies)		(No Copies)	Yes	(No Copies)	Yes	(No Copies)
		w/o VRAM	No	No	(No Copies)	Yes	(No Copies)	No	No	(No Copies)	Yes	(No Copies)		Yes	Yes	(No Copies)	Yes	(No Copies)
	Redistribution Permission	with VRAM	Not Allowed	Not Allowed	Not Allowed	Not Allowed	Not Allowed	Allowed	Allowed	Not Allowed	Not Allowed	Not Allowed		Not Allowed	Not Allowed	Not Allowed	Not Allowed	Not Allowed
		w/o VRAM	Allowed	Allowed	Not Allowed	Not Allowed	Not Allowed	Allowed	Allowed	Invalid RC	Invalid RC	Invalid RC		Not Allowed	Not Allowed	Not Allowed	Not Allowed	Not Allowed
	Copy Permission	with VRAM	Copy Never	Copy Freely	Copy No More	Copy Once	Copy Never	Copy Never	Copy Freely	Copy No More	Copy Once	Copy Never		Copy Never	Copy Freely	Copy No More	Copy Once	Copy Never
		w/o VRAM	Copy Freely	Copy Freely	Copy No More	Copy Once	Copy Never	Copy Freely	Copy Freely	Copy No More	Copy Once	Copy Never		Copy Freely	Copy Freely	Copy No More	Copy Once	Copy Never
Detected State		RC	Z	N	N	N	Z	0	0	0	0	0		1	1	1	1	1
	S-A	P1	N	0	1	0	1	z	0	1	0	1		Z	0	1	0	-
	CGMS-A	P0	N	0	0	1	1	z	0	0	-	-		Z	0	0	1	-

N N 0 is an unlikely state

Key

N = Not present
Y = Present
0 = Bit is 0
1 = Bit is 1
RC = Redistribution Control Descriptor
P0, P1 = CGMS-A bits 1 and 2

FIGURE 4B

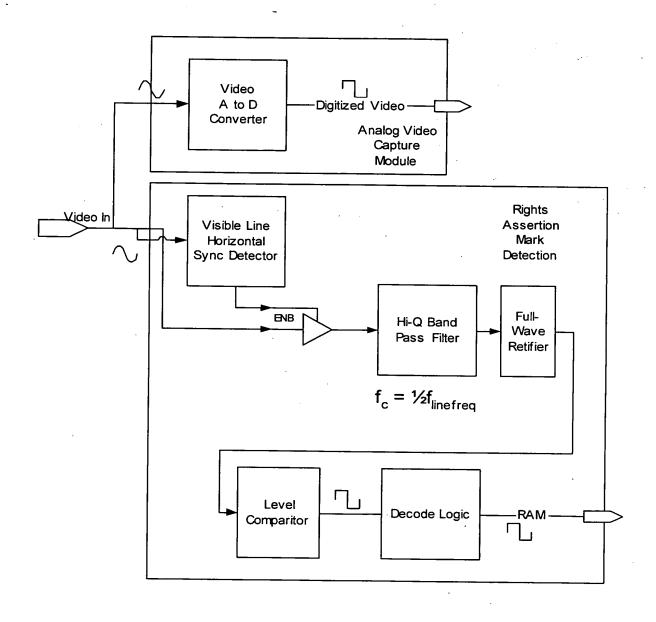


FIGURE 6